Attorney Docket No: 10559-853001/P17234

Abstract of the Disclosure

Systems and techniques to monitor instruction mix in a processor. In general, in one implementation, the technique includes: identifying instruction types of sampled machine instructions being performed by a processor, and presenting a metric indicating utilization of the processor by identified instruction types. Presenting the metric may involve displaying a representation of real-time instruction mix utilization including an indication of a percent of the machine instructions that are optimized for the processor.

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